TOSHIBA RF POWER AMPLIFIER MODULE

S-AV38

RF POWER AMPLIFIER MODULE for VHF BAND

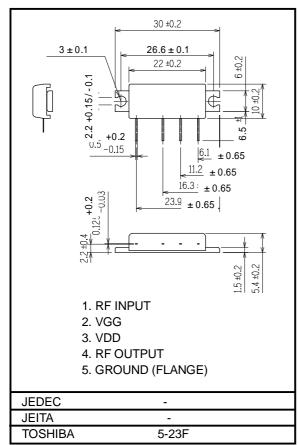
· for digital use

MAXIMUM RATINGS (Tc = 25)

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V _{DD}	17	V
DC Supply Voltage	V _{GG}	7	V
Input Power	Pi	17	dBmW
Operating Case Temperature Range	T _{c (opr)}	-30~100	
Storage Temperature Range	T _{stg}	-40~110	

PACKAGE OUTLINE

Unit in mm



Weight:3.5g

ELECTRICAL CHARACTERISTICS (Tc = 25 , $Z_G = 50$)

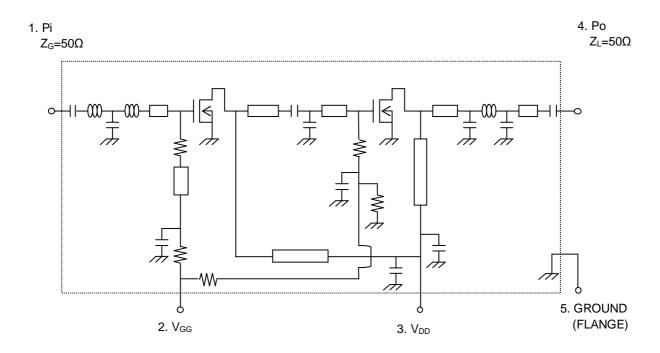
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Frequency Range	f _{range}	—	260	—	266	MHz
Output Power	Po	$\label{eq:VD} \begin{array}{l} V_{DD} = 7.2V, \mbox{ Po=35dBmW}(\mbox{Pi=adjust}) \\ I_{DD} = 1.7A(V_{GG} = adjust) \ , \ Z_L = 50 \\ \mbox{After that Pi} = 15dBmW \end{array}$	38.8	_	_	dBmW
Input Power	Pi	V_{DD} = 7.2V, I_{DD} = 1.7A (V_{GG} = adjust) Po = 35dBmW(Pi=adjust), Z_L = 50	_	_	5	dBmW
Gate Bias Voltage	VGG	$V_{DD} = 7.2V$, $I_{DD} = 1.7A$ ($V_{GG} = adjust$) Po = 35dBmW(Pi=adjust), $Z_L = 50$	2.5	_	3.5	V
Gate Bias Current	IGGBias	V_{DD} = 7.2V, I_{DD} = 1.7A (V_{GG} = adjust) Po = 35dBmW (Pi= adjust), Z_L = 50 After that Pi OFF	_	_	1	mA
Adjacent-Channel Power Ratio	ACP	$\begin{split} V_{DD} &= 7.2 \text{V}, \text{I}_{DD} = 1.7 \text{A} (\text{V}_{GG} = \text{adjust}) \\ \text{Po} &= 35 \text{dBmW} (\text{Pi= adjust}), \text{Z}_{L} = 50 \\ \text{Modulated Wave : } /4 \cdot \text{DQPSK} \\ (\alpha &= 0.5, 32 \text{kbps}) \\ \text{Band Width : 16 \text{kHz}} \\ \text{Frequency Offset : 25 \text{kHz}} \end{split}$	_	_	-35	dB
Second Harmonic	2nd HRM			_	-27	dB
Third Harmonic	3rd HRM	$V_{DD} = 7.2V$, $I_{DD} = 1.7A$ ($V_{GG} = adjust$) Po = 35dBmW (Pi= adjust), $Z_{L} = 50$	_	—	-30	dB
Harmonic	HRM		_	—	-35	dB
Rate of Adjustment for Input Load	VSWRin	Input VSWR (When RF output pin connects 50 Load)	_	_	3	_
Rate of Adjustment for Output Load	VSWRout	Input VSWR (When RF input pin connects 50 Load)		_	2.5	—
Ralative Phase Variation	_	$\label{eq:VDD} \begin{array}{l} V_{DD} = 7.2V, \ I_{DD} = 1.7A \ (V_{GG} = adjust) \\ Po = 5 \ to \ 35dBmW \ (Pi = adjust) \\ Z_L = 50 (\textcircled{e} \ Po = 35dBmW) \end{array}$		_	± 12	o
Load Mismatch	_	$\label{eq:VDD} \begin{array}{l} V_{DD} = 7.2V, \ I_{DD} = 1.7A \ (V_{GG} = adjust) \\ Po = 35dBmW \ (Pi = adjust, \ Z_L = 50 \) \\ VSWR \ LOAD \ 20: \ 1 \ ALL \ PHASE \end{array}$	No Degradation		_	
Stability	_	V _{DD} = 6.0 to 9.0V, V _{GG} = 1 to 5V Pi = -40 to 13 dBmW VSWR LOAD 3: 1 ALL PHASE	All spurious output than 60dB below desired signal		_	

Caution

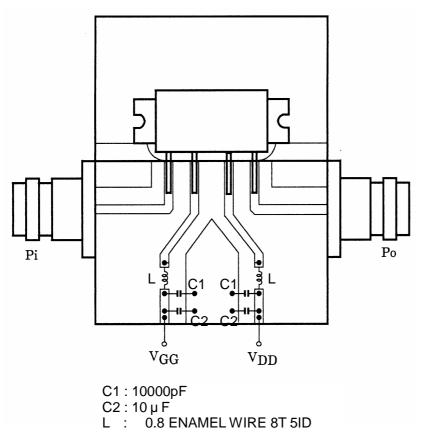
- This product has intersetting cap. Please pay attention for exceeding stress and foreign matter in your application. And not to take away the cap.
- Do not break, cut, crush or dissolve chemically. Dispose of this product properly according to law. Do not intermingle with normal industrial or domestic waste.
- This product is electrostatic sensitivity, please handle with caution.

TOSHIBA

SCHEMATIC



TEST FIXTURE



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